	CD Module Specification	First Edition March 27, 1997 Final Revision	Approved by Checked by	Production Div. Quality Assurance Div.
		*****	Checked by	Design Engineering Div.
Type No.	DMF5002NY-EB		Prepared by	Production Div.
	1. General Specifications.2. Electrical Specifications.3. Optical Specifications.4. I/O Terminal.5. Test.6. Appearance Standards.7. Code System of Production Lot8. Type Number.9. Applying Precautions.10. Handling Precautions.			3 7 9 .11 12 15 15

# Revision History

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# 1.General Specifications

Operating Temp.	:	min. 0°C ~ max. 50°C
Storage Temp.	:	min20°C ~ max. 60°C
Dot Pixels	:	128 (W) × 112 (H) dots
Dot Size	:	0.50 (W) × 0.49 (H) mm
Dot Pitch	:	0.54 (W) × 0.53 (H) mm
Viewing Area	:	77.0 (W) × 66.0 (H) mm
Outline Dimensions	:	110.0 (W) × 90.6 (H) × 12.8 (D) mm
Weight	:	120g max.
LCD Type	:	NSD-7399 (STN / Yellow-mode / Transflective)
Viewing Angle	:	3:00
Control LSI	:	T6963C-0101 (Produced by TOSHIBA)
Data Transfer	:	8-bit parallel data transfer
Backlight	:	Electro Luminescence (EL) / Blue-green
Drawings	:	Dimensional Outline UE-35450

# **2. Electrical Specifications**

### 2.1. Absolute Maximum Ratings

					Vss=0V
Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage	Vcc-Vss	-	-0.3	7.0	v
(Logic)					
Supply Voltage	VCC-VEE	_	0	30.0	v
(LCD Drive)					
Input Voltage	VI	_	-0.3	Vcc+0.3	V

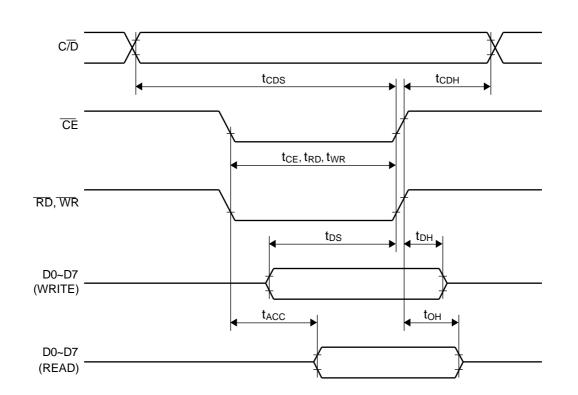
### 2.2.DC Characteristics

Ta=25°C, Vss=0V

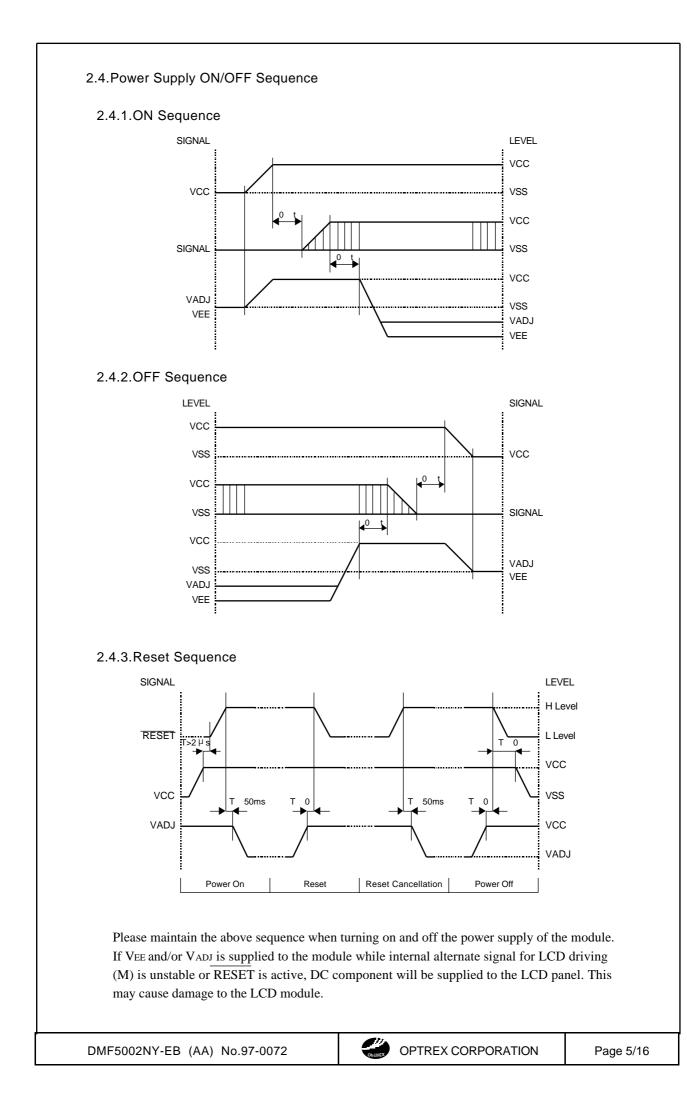
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage (Logic)	Vcc-Vss	-	4.5	_	5.5	V
Supply Voltage	VCC-VEE	-	21.0	_	26.0	V
(LCD Drive)	Vcc-Vadj		Shown in 3	.1		V
High Level Input Voltage	Vih	Vcc=5.0V±10%	Vcc-2.2	_	Vcc	V
Low Level Input Voltage	VIL	Vcc=5.0V±10%	0	_	0.8	V
High Level Output Voltage	Vон	Іон=-0.75тА	Vcc-0.3	-	Vcc	V
Low Level Output Voltage	Vol	Iol=0.75mA	0	_	0.3	V
Supply Current	Icc	Vcc-Vss=5.0V	_	9.0	20.0	mA
	IEE	Vcc-Vadj=18.9V	_	4.0	10.0	mA

### 2.3.AC Characteristics

	Vcc=5.0			=5.0V±10%
Parameter	Symbol	Min.	Max.	Units
C/D Setup Time	tcds	100	_	ns
C/D Hold Time	tcdh	10	_	ns
$\overline{\text{CE}}, \overline{\text{RD}}, \overline{\text{WR}}$ Pulse Width	$t_{CE}, t_{RD}, t_{WR}$	80	_	ns
Data Setup Time	tds	80	_	ns
Data Hold Time	<b>t</b> dh	40	_	ns
Access Time	tacc	_	150	ns
Output Hold Time	tон	10	50	ns



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# 2.5.Lighting Specifications

### 2.5.1. Absolute Maximum Rating

Ta=25°C

Parameter	Conditions	Min.	Тур.	Max.	Units
Input Voltage	-	_	Ι	150	Vrms
Input Frequency	AC 100Vrms	—	_	800	Hz

#### 2.5.2. Operating Characteristics

					Ta=25°C
Parameter	Conditions	Min.	Тур.	Max.	Units
Input Voltage	_	_	100	Ι	Vrms
Input Frequency	_	_	400	Ι	Hz
Current	AC 400Vrms, 100Hz	_	9.4	12.0	mA
Luminance of	AC 400Vrms, 100Hz	40	50	-	cd/m <sup>2</sup>
Backlight Surface					
Life	AC 400Vrms, 100Hz	2000	_	_	hrs
	Ta=20°C, 60%RH				

## **3.Optical Specifications**

#### 3.1.LCD Driving Voltage

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Recommended		Ta= 0°C	Ι	_	22.5	v
LCD Driving Voltage	VCC-VADJ	Ta=25°C	17.6	18.9	20.2	V
Note 1		Ta=50°C	15.9	—	-	V

Note 1 : Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2. Optical Characteristics

		Ta=2	25°C, 1/128 Duty, 1/11.6	5 Bias, VD	=18.3V (N	ote 4), $=$	$0^\circ$ , $=$ $-^\circ$
Parameter		Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast R	atio Note 1	CR	$=0^{\circ}, =-^{\circ}$	_	8	_	
Viewing Angle Shown in 3.3							
Response	Rise Note 2	r	-	Ι	200	300	ms
Time	Decay Note 3	d	_	_	340	510	ms

Note 1 : Contrast ratio is definded as follows.

CR = LOFF / LON

LON: Luminance of the ON segments

LOFF: Luminance of the OFF segments

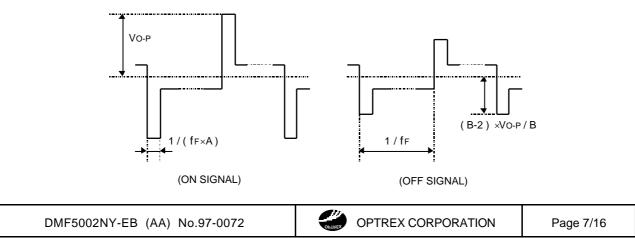
- Note 2 : The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.
- Note 3 : The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

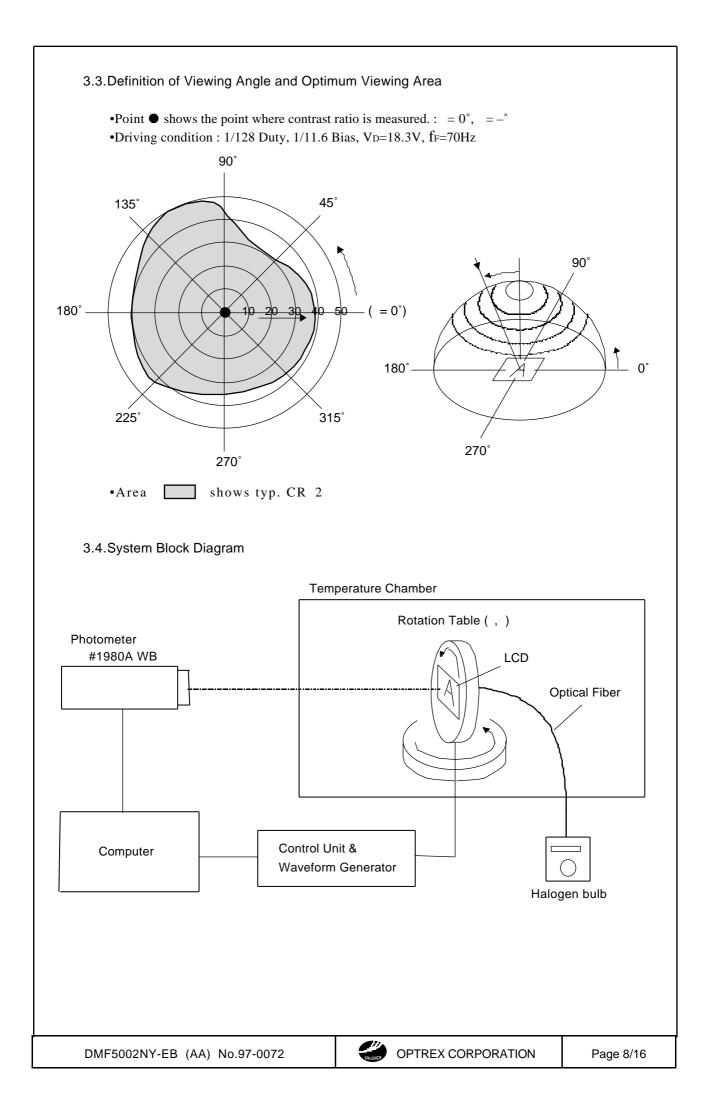
Note 4 : Definition of Driving Voltage VD

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias (A : Duty Number, B : Bias Number ). Driving voltage VD is definded as follows.

 $V_D = (Vth1+Vth2) / 2$ 

- Vth1 :The voltage Vo-P that should provide 50% of the satulation level in the luminance at the segment which the ON signal is applied to.
- Vth2 :The voltage Vo-P that should provide 50% of the satulation level in the luminance at the segment which the OFF signal is applied to.





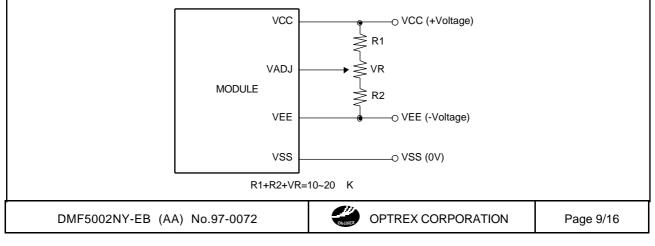
## 4.I/O Terminal

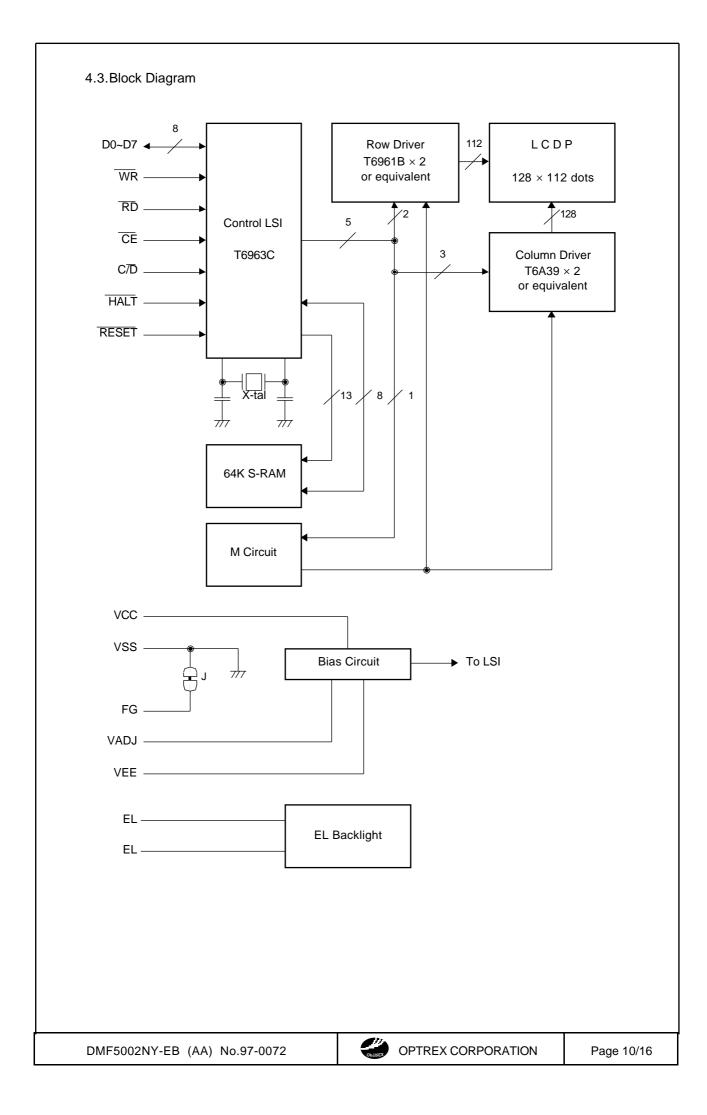
#### 4.1.Pin Assignment

No.	Symbol	Level	Function
1	FG	_	Frame Ground
2	Vss	_	Power Supply (0V, GND)
3	Vcc	_	Power Supply for Logic
4	Vadj	_	Voltage Level for LCD Contrast Adjustment
5	VEE	_	Power Supply for LCD Drive
6	WR	H/L	Write Signal L : Active
7	RD	H/L	Read Signal L : Active
8	CE	H/L	Chip Enable Signal L : Active
9	C/D	H/L	Write Mode H : Command Write L : Data Write
			Read Mode H : Status Read L : Data Read
10	HALT	H/L	Clock Operating Stop Signal L : Halt
11	RESET	H/L	Reset Signal L : Reset
12	D0	H/L	Display Data
13	D1	H/L	Display Data
14	D2	H/L	Display Data
15	D3	H/L	Display Data
16	D4	H/L	Display Data
17	D5	H/L	Display Data
18	D6	H/L	Display Data
19	D7	H/L	Display Data
20	NC	_	Non-connection
21	EL	_	Power Supply for EL
22	EL	_	Power Supply for EL

#### 4.2. Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.





### <u>5.Test</u>

No change on display and in operation under the following test condition.

No.	Parameter	Conditions	Notes
1	High Temperature Operating	50°C±2°C, 96hrs (operation state)	
2	Low Temperature Operating	0°C±2°C, 96hrs (operation state)	3
3	High Temperature Storage	60°C±2°C, 96hrs	4
4	Low Temperature Storage	-20°C±2°C, 96hrs	3, 4
5	Damp Proof Test	40°C±2°C, 90~95%RH, 96hrs	3, 4
6	Vibration Test	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes	5
7	Shock Test	To be measured after dropping from 60cm high on the concrete surface in packing state. $ \begin{array}{c}                                     $	

Note 1 : Unless otherwise specified, tests will be conducted under the following condition. Temperature  $~:20{\pm}5^{\circ}C$ 

Humidity : 65±5%

Note 2 : Unless otherwise specified, tests will be not conducted under functioning state.

Note 3 : No dew condensation to be observed.

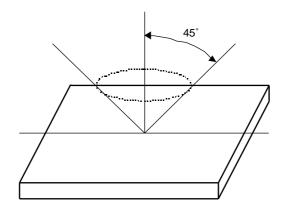
Note 4 : The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5 : Vibration test will be conducted to the product itself without putting it in a container.

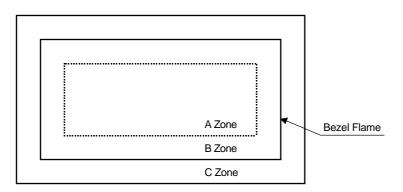
## 6. Appearance Standards

#### 6.1.Inspection conditions

The LCD shall be inspected under 40W white fluorescent light. The distance between the eyes and the sample shall be more than 30cm. All directions for inspecting the sample should be within 45° against perpendicular line.



6.2. Definition of applicable Zones



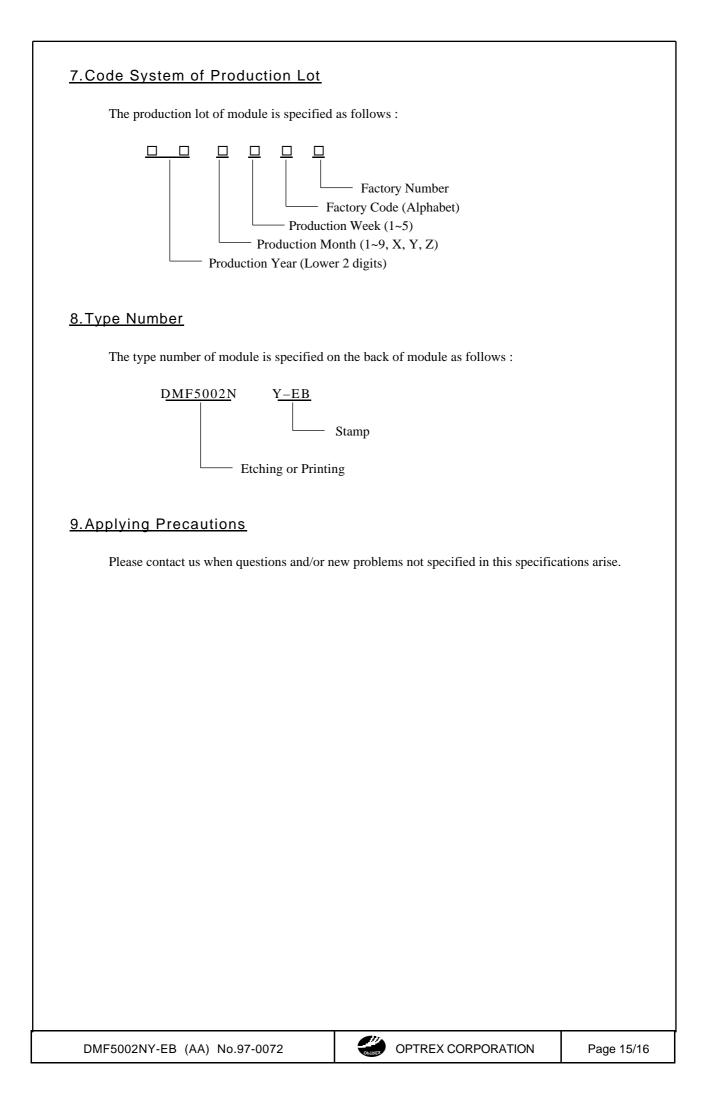
A Zone : Active display area

B Zone : Area from outside of "A Zone" to validity viewing area C Zone : Rest parts

A Zone + B Zone = Validity viewing area

lo.	Parameter					
1	Black and	(1) Round Shape				
	White Spots,	Zone	A	Acceptable Number		
	Foreign Substances	Dimension (mm)	А	В	С	
		D 0.1	*	*	*	
		0.1 < D 0.2	3	5	*	
		0.2 < D 0.25	2	3	*	
		0.25< D 0.3	0	1	*	
		0.3 < D	0	0	*	
		D = (Long + Short) / 2 * : Disregard (2) Line Shape				
		Zone	Acceptable Number			
		X (mm) Y (mm)	А	В	С	
		– 0.03 W	*	*	*	
		2.0 L 0.05 W	3	3	*	
		1.0 L 0.1 W	3	3	*	
		– 0.1 <w< td=""><td colspan="3">In the same way (1)</td></w<>	In the same way (1)			
2	Air Bubbles (between glass	Total defects shall not exceed 5.				
	& polarizer)	Dimension (mm)	A	В	С	
	1 /	D 0.3	*	*	*	
		0.3 < D 0.4	3	*	*	
		0.4 < D 0.6	2	3	*	
		0.6 < D	0	0	*	
		* : Disregard				
		Total defects shall not exceed 3.				
	•	•				

No.	Parameter	Criteria			
3	The Shape of Dot	(1) Dot Shape (with Dent) 0.15 As per the sketch of left hand.			
	(2) Dot Shape (with Projection)				
		Should not be connected to next dot.			
		(3) Pin Hole (3) Pin Hole (X+Y)/2  0.2mm (Less than $0.1mm$ is no counted.)			
		(4) Deformation			
		(X+Y)/2  0.2mm Total acceptable number : 1/dot, 5/cell (Defect number of (4) : 1pc.)			
4	Polarizer Scratches	Refer to the sample.			
5	Polarizer Dirts	If the stains are removed easily from LCDP surface, the module is not defective.			
6	Complex Foreign Substance Defects	Black spots, line shaped foreign substances or air bubbles between glass & polarizer should be 5pcs maximum in total.			
7	Distance between Different Foreign Substance Defects	D 0.2 : 20mm or more 0.2 <d 40mm="" :="" more<="" or="" td=""></d>			



### 10.Handling Precautions

Operex Products are designed for use in ordinary electronic devices such as business machines, telecommunications equipment, measurement devices and etc..

Optrex Products are not designed, intended, or authorized for use in any application in which the failure of the product could result in a situation where personal injury or death may occur. These applications include, but are not limited to, life-sustaining equipment, nuclear control devices, aerospace equipment, devices related to hazardous or flammable materials, etc. (If Buyer intends to purchase or use the Optrex Products for such unintended or unauthorized applications, Buyer must secure prior written consent to such use by a responsible officer of Optrex Corporation.) Should Buyer purchase or use Optrex Products for any such unintended or unauthorized application (without such consent), Buyer shall indemnify and hold Optrex and its officers, employees, subsidiaries, affiliates and distributors harmless against all claims, costs, damages and expenses, and reasonable attorney's fees, arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Optrex was negligent regarding the design or manufacture of the part.

- 1) LCD may be broken because it is made of glass.
- 2) Polarizer is a soft material and can easily be scratched.
- 3) Please avoid static electricity.
- ① Please be sure to ground human body and electric appliances during work.
- ② It is preferable to use conductive mat on table and wear cotton clothes or conduction processed fiber. Synthetic fiber is not recommended.
- ③ Please slowly peel off protective film, because static electricity may be charged.
- 4) If it is necessary to store LCD modules for a long time, please comply with the following procedures. If storage condition is not satisfactory, display (especially polarizer) may be deteriorated or soldering I/O terminals may become difficult (some oxide is generated at I/O terminals plating).
  - ① Store as delivered by Optrex
  - ② If you store as unpacked, put in anti-static bag, seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
  - ③ Store at temperature 0 to +35°C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.
- The module does not contain excess current limiter.
   Please design the limiter to cut excess current in your power supply circuit.
- 6) Liquid crystal may be leaked when display is broken. Never taste it. If your hands or clothes touch it, please immediately wash using soap.
- The connection between the bezel and Vss (GND) is not specified in the module. (Some module do not maintain connection between them.) Please consult OPTREX to specify the connection.

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